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(71) Applicant: TELEFONAKTIEBOLAGET LM ERICSSON (publ) [SE/SE]; S-126 25 Stockholm (SE).

(72) Inventors: CARLSSON, Erik; Toltorpsgatan 39C, S-431 39 Mölndal (SE). PETROV, Peter; Chalmers Tekniska Högskola, Mikrloelektronik, S-412 96 Göteborg (SE). VENDIK, Orest; Korabelostroitelay 42, apt, S.Petersburg, 199155 (RU). WIKBORG, Erland; Lundblads väg 3, S-182 33 Danderyd (SE). IVANOV, Zdravko; Mölndalsvägen 41, S-412 63 Göteborg (SE).

(74) Agents: BERGENTALL, Annika et al.; Cegumark AB, P.O. Box 53047, S-400 14 Göteborg (SE).

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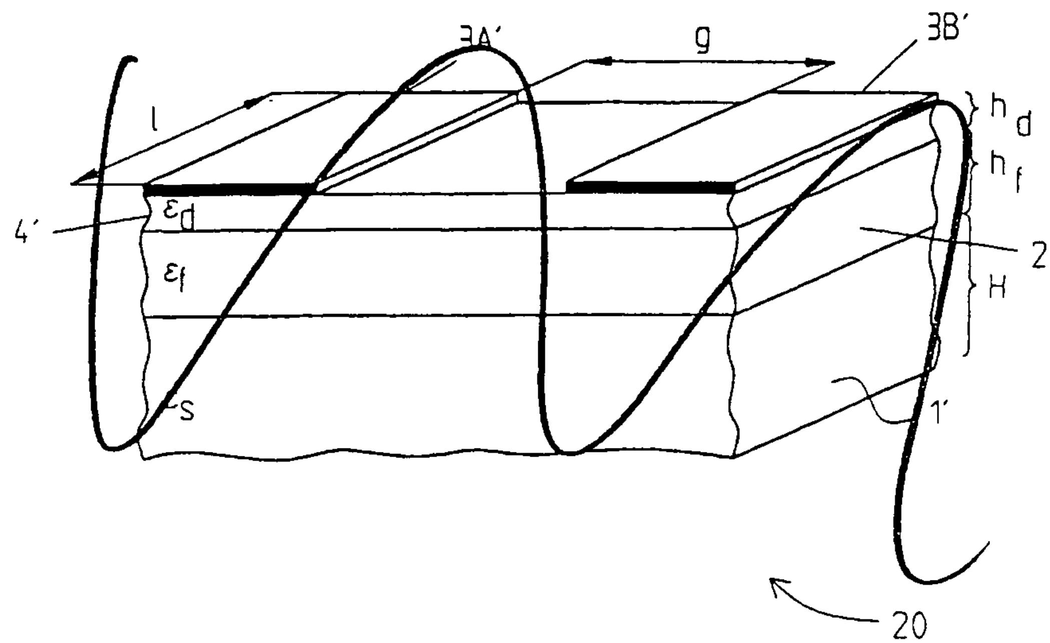

(57) Abstract

The present invention relates to an electrically tunable device (10), particularly for microwaves. It comprises a carrier substrate

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E29 P66PCT AB/ej 00-04-06

Title:

TUNABLE MICROWAVE DEVICES

FIELD OF THE INVENTION

The present invention relates to electrically tunable devices particularly for microwaves, which are based on a ferroelectric structure.

STATE OF THE ART

Known electrically tunable devices, such as capacitors (varactors) and which are based on ferroelectric structures do indeed have a high tuning range but the losses at microwave frequencies are high thus limiting their applicability. Typical ratios between the maximum and the minimum values of the dielectric constant (without and with applied electric fields) ranges from n=1.5 to 3 and the loss tangents ranges from 0.02 to 0.05 at 10 GHz. This is not satisfactory for microwave applications requiring a low loss. Then e.g. a quality factor of about 1000-2000 is needed. WO 94/13028 discloses a tunable planar capacitor with ferroelectric layers. However, the losses are high at microwave frequencies.

US-A-5 640 042 shows another tunable varactor. Also in this case the losses are too high Losses across the interface dielectric material-conductor are produced which are high and furthermore the free surface between the conductors results in the ferroelectric material being exposed during processing (e.g. etching, patterning) which produce losses since the crystal structure can be damaged.

SUMMARY OF THE INVENTION

What is needed is therefore a tunable microwave device having a high tuning range in combination with low losses at microwave frequencies. A device is also needed which has a quality factor at microwave frequencies such as for example up to 1000-2000. A device is also needed in which the ferroelectric layer is stabilized and a device which shows a performance which is stable with the time, i.e. the performance does not vary and become deteriorated with time.

Furthermore a device is needed which is protected against avalanche electric breakdown in the tunable ferroelectric material.

Further yet a device is needed which is easy to fabricate. A device is also needed which is insensitive to external factors as temperature, humidity etc. Therefore an electrically tunable device, particularly for microwaves, is provided which comprises a carrier substrate, conducting means and at least one tunable ferroelectric layer. Between the/each (or at least a number of) conducting means and a tunable ferroelectric layer a buffer layer structure is provided which comprises a thin film structure comprising a non-ferroelectric material.

According to one embodiment the thin film structure comprises a thin non-ferroelectric layer. In an alternative embodiment the thin film structure comprises a multi-layer structure including a number of non-ferroelectric layers. In still further embodiments a multilayer structure including a number of non-ferroelectric layers arranged in an alternating manner with ferroelectric layers (such that a non-ferroelectric layer always is provided adjacent the/a conducting means.

In a particular embodiment the ferroelectric layer is arranged on top of the carrier substrate and the non-ferroelectric thin film structure, including one or more layers, is arranged on top of the ferroelectric layer the conducting means in turn being arranged on top of the non-ferroelectric structure. In an alternative embodiment the ferroelectric layer is arranged above the non-ferroelectric structure including one or more non-ferroelectric layers, which is arranged on top of the conducting means. The conducting means particularly comprise (at least) two longitudinally arranged electrodes between which electrodes or conductors a gap is provided. According to different embodiments the non-ferroelectric structure is deposited in-situ on the ferroelectric layer or deposited ex-situ on the ferroelectric layer.

The deposition of the non-ferroelectric layer may be performed using different techniques such as for examples laser deposition, sputtering, physical or chemical vapour deposition or through the use of sol-gel techniques. Of course also other techniques which are suitable can be used.

Advantageously the ferroelectric and the non-ferroelectric structures have lattice matching crystal structures. The non-ferroelectric structure is particularly arranged so as to cover also the gap between the conductors or the electrodes. In a particular implementation the device comprises an electrically tunable capacitor or a varactor.

In another embodiment the device includes two layers of ferroelectric material provided on each side of the carrier substrate and two conducting means, non-ferroelectric thin film structures being arranged between the respective ferroelectric and non-ferroelectric structures in such a way that the device

forms a resonator. According to different implementations the device of the invention may comprise microwave filters or be used in microwave filters. Also devices such as phase shifters etc. can be provided using the inventive concept

Different materials can be used; one example of a ferroelectric material is STO ($SrTiO_3$). The non-ferroelectric material may for example comprise CeO_2 or a similar material or $SrTiO_3$ which is doped in a such a way that it is not ferroelectric. An advantageous use of a device as disclosed is in wireless communication systems.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will in the following be further described in a non-limiting way and with reference to the accompanying drawings in which:

Fig 1 shows a cross-sectional view of a tunable device according to a first embodiment of the invention,

Fig 2 schematically illustrates a planar capacitor similar to the embodiment of Fig 1,

Fig 3 shows a second embodiment of an inventive device,

Fig 4 shows still another embodiment in which a structure comprising alternating layers is used,

Fig 5 illustrates a fourth embodiment of a device according to the invention,

Fig 6 schematically illustrates an experimental dependence of the tunability as a function of the capacitance for a number of material thicknesses, and

Fig 7 shows the experimental results relating to the loss factor when using a non-dielectric layer according to the invention.

DETAILED DESCRIPTION OF THE INVENTION

Through the invention devices are disclosed through which it is possible to achieve a high tunability in combination with low losses at microwave frequencies. In general terms this is achieved through a design in which a thin non-ferroelectric, dielectric layer (or layers) is (are) arranged between the conducting layer and a tunable ferroelectric layer. The non-ferroelectric layer will also act as a cover for the ferroelectric layer in the gap between the conducting means or the electrodes. The non-ferroelectric layer can be deposited "in-situ" or "ex-situ" on the ferroelectric layer by laser deposition, sputtering, physical vapour deposition, chemical vapour deposition, sol-gel or any other convenient technique. The non-ferroelectric layer should be oriented and have a good lattice match to the crystal structure of the ferroelectric layer. Further it should have low microwave losses. In all embodiments as referred to below or not explicitly disclosed, the non-ferroelectric layer structure may be a single layered structure or it may comprise a multilayered structure.

The thin non-ferroelectric structure will reduce the total capacitance of the device due to the presence of two capacitances of the thin non-ferroelectric structures in series with the tunable capacitance resulting from the ferroelectric layer. Even if the total capacitance is reduced, which is wanted

in most applications, the tunability will only decrease slightly since the change in the dielectric constant of the ferroelectric layer will redistribute the electric field and change the series capacitances due to the thin non-ferroelectric structure.

Fig. 1 shows a first embodiment of a device 10 according to the invention which comprises a substrate 1 or which a ferroelectric material 2, which is tunable, is provided. On said tunable ferroelectric material 2, a non-ferroelectric layer 4 is deposited, for example using any of the techniques as referred to above. Two conducting means comprising a first conductor or electrode 3A and a second conductor or electrode 3B are arranged on the non-ferroelectric layer 4. Between the first and second electrodes 3A, 3B there is a gap. As can be seen from the figure the non-ferroelectric structure 4 covers the tunable ferroelectric structure 2 across the gap between the conductors 3A, 3B. The surface of the ferroelectric structure 4 is thus protected by the non-ferroelectric structure 4 in a finished state but also during processing, i.e. when the device is fabricated. Since the ferroelectric structure 2 is protected in this manner, the ferroelectric structure will be stabilized and its performance will be stable with the time, i.e. it does not deteriorate with the time. Furthermore the losses will decrease since there will be a higher control of the interface of the ferroelectric structure and there will be less defects on the surface layer of the ferroelectric material. Instead of two electrodes, the conducting means may include more than two electrodes e.g. one or more electrodes provided between the electrodes 3A,3B.

Furthermore the non-ferroelectric layer will provide a protection against avalanche electric breakdown in the tunable ferroelectric material.

Although the non-ferroelectric structure 4 is shown as comprising a merely one layer, it should be clear that it also may comprise a multilayer structure.

Fig 2. shows an embodiment relating to a planar capacitor 20. Relating to this embodiment some figures are given relating to dimensions, values etc. which here of course only are given for illustrative purposes. The device includes a substrate 1′ for example of $LaAlO_3$ having a thickness H of for example 0.5 mm, and with a dielectric permittivity ε_s=25. On top of the substrate a ferroelectric layer 2′ for example of STO is arranged which here has a thickness h_f of 0.25μm and with a dielectric permittivity ε_f=1500. Thereon the protective buffer layer 4′, which is a non-ferroelectric e.g. dielectric layer, is arranged having a dielectric permittivity ε_d=10.

In Fig. 3 an alternative device 30 is disclosed in which a non-ferroelectric structure 4″, here comprising a multiple of sublayers, are arranged on top of conducting electrodes, 3A′, 3B′ which are arranged on substrate 1″. The non-ferroelectric multilayer structure is deposited on (below) a tunable ferroelectric material 2″. The functioning is substantially the same as that as described with reference to Fig. 1, only it is an inverted structure as the ferroelectric is arranged above the non-ferroelectric layer, i.e. above the electrodes. Furthermore the non-ferroelectric layer comprises a multilayer structure. Of course in this embodiment the non-ferroelectric structure may alternatively comprise a single layer.

Fig 4 shows a tunable capacitor 40 in which a structure comprising ferroelectric layers $2A_1$, $2A_2$, $2A_3$ and non-ferroelectric layers $4A_1$, $4A_2$, $4A_3$ which are arranged in an

alternating manner. The number of layers can of course be any and is not limited to three of each kind as illustrated in Fig. 4, the main thing being that a non-ferroelectric layer (here $4A_1$) is arranged in contact with the conducting means $3A_1$, $3B_1$; also covering a ferroelectric layer (here $2A_1$) in the gap between the electrodes.

Such an alternating arrangement can of course also be used in the "inverted" structure as disclosed in Fig. 3.

Fig. 5 shows yet another device 50 in which first conducting means $3A_2$, $3B_2$ in the form of electrodes are arranged on a non-ferroelectric layer 4C, which in turn is deposited on a ferroelectric, active, layer 2C. Below the ferroelectric layer 2C a further non-ferroelectric layer 4D is provided on the opposite side of which second conducting means $3A_3$, $3B_3$ are arranged, which in turn are arranged on a substrate 1C. Also in this case may an alternating structure as in Fig. 4 be used.

Any of the materials mentioned above can be used also in these implementations. The non-ferroelectric material can be dielectric, but it does not have to be such a material. Still further it may be ferromagnetic.

The active ferroelectric layer structure of any embodiment may for example comprise any of $SrTiO_3$, $BaTiO_3$, $Ba_xSr_{1-x}TiO_3$, PZT (Lead Zirconate Titanate) as well as ferromagnetic materials. The buffer layer or the protective non-ferroelectric structure may e.g. comprise any of the following materials: CeO_2, MgO, YSZ (Ytterium Stabilized Zirconium), $LaAlO_3$ or any other non-conducting material with an appropriate crystal structure, for example PrBCO ($PrBa_2Cu_3O_{7-x}$), non-conductive $YBa_2Cu_3O_{7-x}$ etc. The substrate may comprise $LaAlO_3$, MgO, R-cut or M-cut sapphire,

$SiSrRuO_3$ or any other convenient material. It should be clear that the lot of examples is not exhaustive and that also other possibilities exist.

In Fig. 6 the dynamic capacitance is illustrated as a function of the voltage for three different thicknesses of the non-ferroelectric buffer layer 4′ which here is dielectric. In this case the length of the planar capacitor is supposed to be 0.5 mm whereas the gap between the conductors 3A′, 3B′ is 4μm. A magnetic wall can be said to be formed between the substrate and the ferroelectric layer 2′.

The capacitance is illustrated as a function of the voltage applied between the electrodes for three different values, namely h_{10}=10nm, h_{30}=30mm and h_{100}=100nm of the dielectric non-ferroelectric buffer layer 4′. The capacitance is also illustrated for the case when there is no buffer layer between the conducting means and the ferroelectric layer, curve h_0. This is thus supposed to illustrate how the tunability is reduced through the introduction of a buffer layer 4′ for a number of thicknesses as compared to the case when there is no buffer layer. As can be seen the reduction in tunability is not significant.

Fig. 7 shows the Q value for a capacitance depending on voltage when a buffer layer is provided, corresponding to the upper curve A, and the case when there is no buffer layer, corresponding to the lower curve B. Thus, as can be seen from the experimental behavior, the Q value for a capacitor is considerably increased through the introduction of a buffer layer.

In addition to the advantages as already referred to above, it is an advantage in using a buffer layer across the active (tunable) ferroelectric layer since when a conductive pattern is etched, some etching will also occur in the subsequent, underlying, layer. Thus damages may be produced in the top layer of the ferroelectric material in the gap if it is not protected.

The inventive concept can also be applied to resonators, such as for example the ones disclosed in "Tunable Microwave Devices" which is a Swedish patent application with application No. 9502137-4, by the same applicant, which hereby is incorporated herein by reference. The inventive concept can also be used in microwave filters of different kinds. A number of other applications are of course also possible. As in other aspects the invention is not limited to the particularly illustrated embodiments but can be varied in a number of ways within the scope of the claims.

CLAIMS

1. An electrically tunable device (10;20;30;40;50), e.g. for microwaves, comprising a carrier substrate (1;1';1";1A-1C), conducting means ($3A,3B;3A',3B';3A'',3B'';3A_1,3B_1;3A_2,3B_2;3A_3,3B_3$) and at least one active ferroelectric layer ($2;2';2'';2A_1,2A_2,2A_3$),

characterized in that

between at least a number of conducting means ($3A,3B;3A',3B';3A'',3B'';3A_1,3B_1;3A_2,3B_2;3A_3,3B_3$) and a ferroelectric layer ($2;2';2'';2A_1,2A_2,2A_3$) a buffer layer ($4;4';4'';4A_1,4A_2,4A_3;4C,4D$) consisting of a thin film structure comprising a non-ferroelectric material is arranged.

2. A device according to claim 1,

characterized in that

the thin film structure ($4;4';4'';4A_1,4A_2,4A_3;4C,4D$) comprises a thin non-ferroelectric layer.

3. A device according to claim 1,

characterized in that

the thin film structure comprises a multi-layer structure ($4'';4A_1,4A_2,4A_3$) including a number of non-ferroelectric layers.

4. A device according to claim 2 or 3,

characterized in

that a number of ferroelectric layers ($2A_1,2A_2,2A_3$) and non-ferroelectric layers ($4A_1,4A_2,4A_3$) are arranged in an alternative manner adjacent to the conducting means ($3A_1,3B_1$).

5. A device according to any one of claims 1-3,

characterized in that

the ferroelectric layer (2;2';$2A_3$) is arranged on top of the carrier substrate (1;1';1A), the non-ferroelectric thin film structure (4;4';$4A_1$) being arranged on top of the ferroelectric layer and in that the conducting means (3A,3B;3A',3B';$3A_1$,$3B_1$) are arranged on top of the non-ferroelectric structure.

6. A device according to any one of claims 1-3, c h a r a c t e r i z e d i n t h a t the ferroelectric layer (2") arranged above the non-ferroelectric structure (4") which is arranged on top of the conducting means (3A",3B") being arranged on the substrate.

7. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t the conducting means comprise two longitudinally arranged electrodes (3A,3B;3A',3B';3A",3B";$3A_1$,$3B_1$;$3A_2$,$3B_2$;$3A_3$,$3B_3$) between which a gap is provided.

8. A device according to any one of claims 1-4, c h a r a c t e r i z e d i n that second conducting means ($3A_3$,$3B_3$) are provided and in that a non-ferroelectric layer (4D) is arranged between said second conducting means ($3A_3$,$3B_3$) and the ferroelectric layer (2C).

9. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t the non-ferroelectric buffer layer structure is deposited in-situ on the ferroelectric layer.

10. A device according to any one of claims 1-6, c h a r a c t e r i z e d i n t h a t the non-ferroelectric buffer layer structure is deposited ex-situ on the ferroelectric layer.

11. A device according to claim 7 or 8, c h a r a c t e r i z e d i n t h a t the non-ferroelectric buffer layer structure is deposited through the use of laser deposition, sputtering, physical or chemical vapour deposition or sol-gel techniques.

12. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t the ferroelectric and the non-ferroelectric structures have lattice matching crystal structures.

13. A device at least according to claim 7, c h a r a c t e r i z e d i n t h a t the non-ferroelectric buffer layer structure (3A,3B;3A',3B';3A",3B";$3A_1$,$3B_1$;$3A_2$,$3B_2$;$3A_3$,$3B_3$) is arranged to cover the gap between the conductors/electrodes.

14. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t it comprises an electrically tunable capacitor (varactor).

15. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t it comprises two layers of a ferroelectric material provided on each side of the carrier substrate and two conducting means, non-ferroelectric thin film structures being arranged between the respective ferroelectric and non-ferroelectric structures, the device forming a resonator.

16. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n

that the non-ferroelectric material of the buffer layer structure is a dielectricum.

17. A device according to any one of claims 1-16, c h a r a c t e r i z e d i n that the non-ferroelectric material is ferromagnetic.

18. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t it is used in microwave filters.

19. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t the ferroelectric material comprises STO ($SrTiO_3$).

20. A device according to any one of the preceding claims, c h a r a c t e r i z e d i n t h a t the non-ferroelectric material comprises CeO_2 or a similar material or $SrTiO_3$ doped in a such a way that it is not ferroelectric.

21. Use of a device as in any one of the preceding claims in wireless communication system.

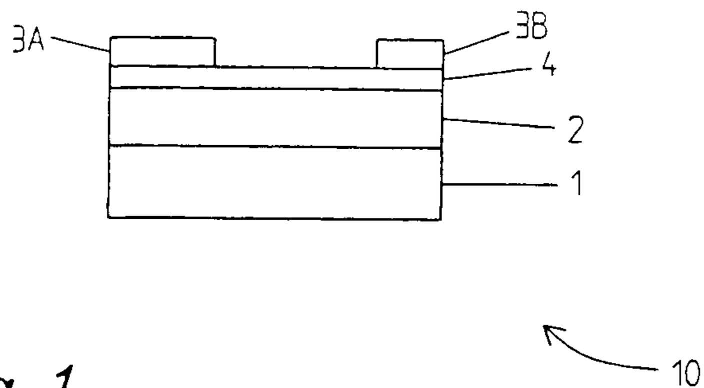
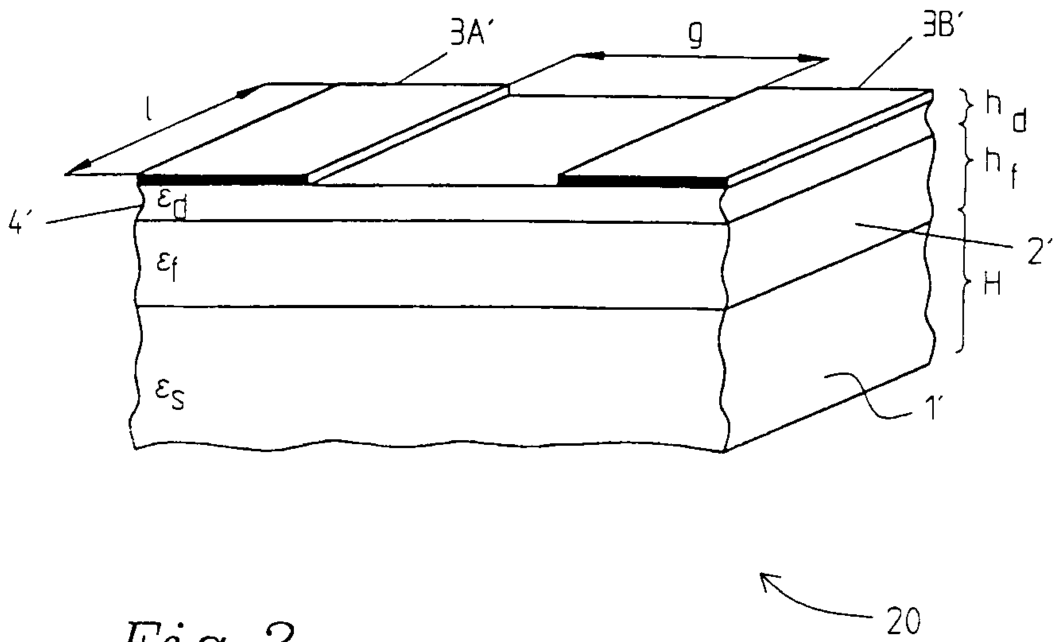

3A
3B
4
2
1
10

Fig. 1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3B'
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h d
h f
H
4'
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ε_s
2'
1'
20

Fig. 2

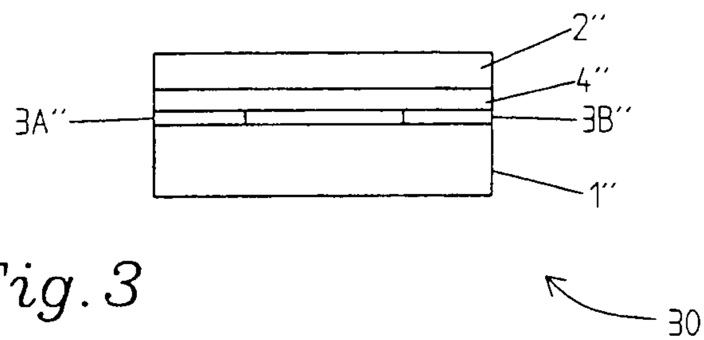

Fig. 3

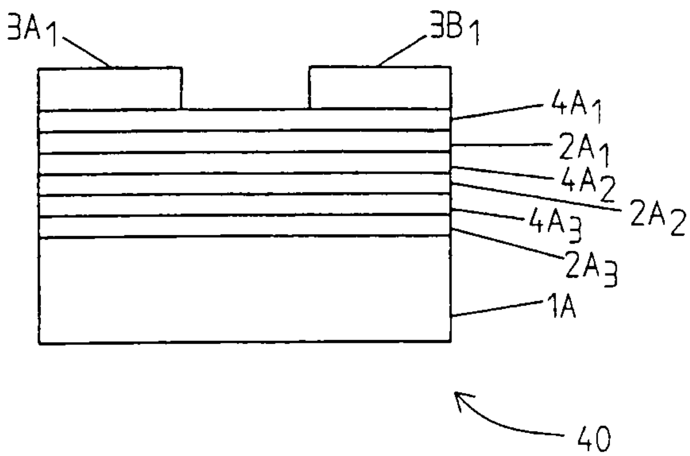

Fig. 4

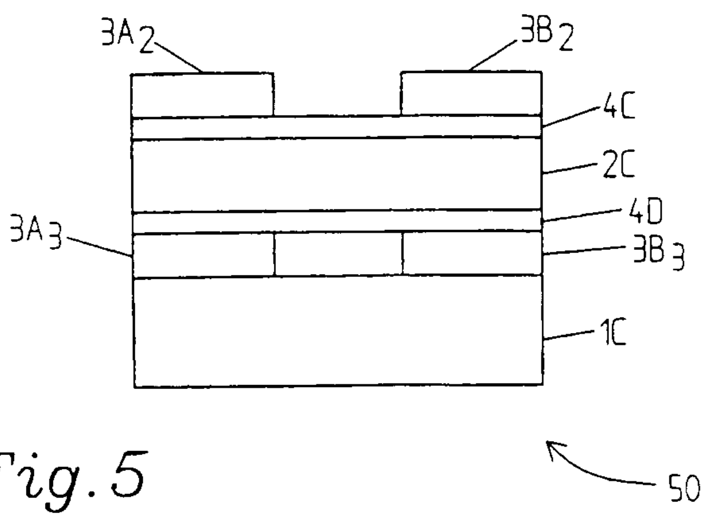

Fig. 5

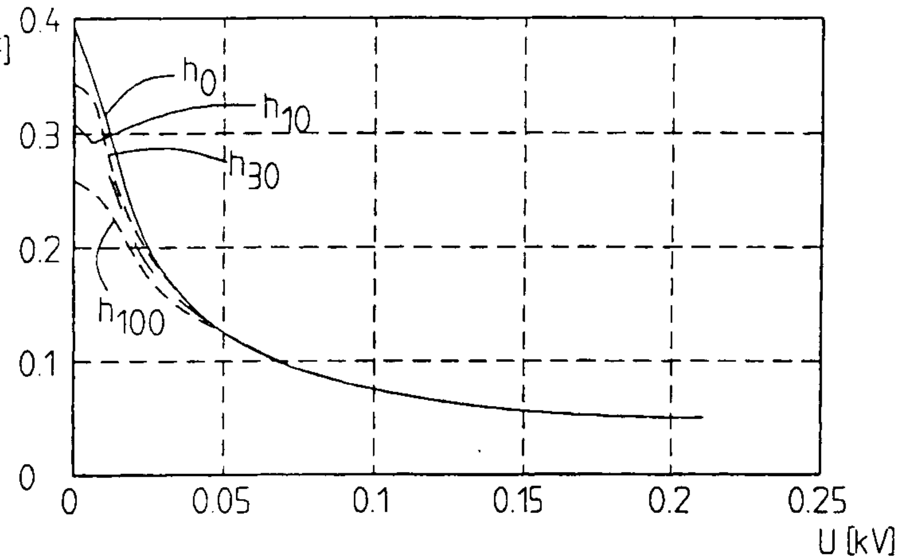
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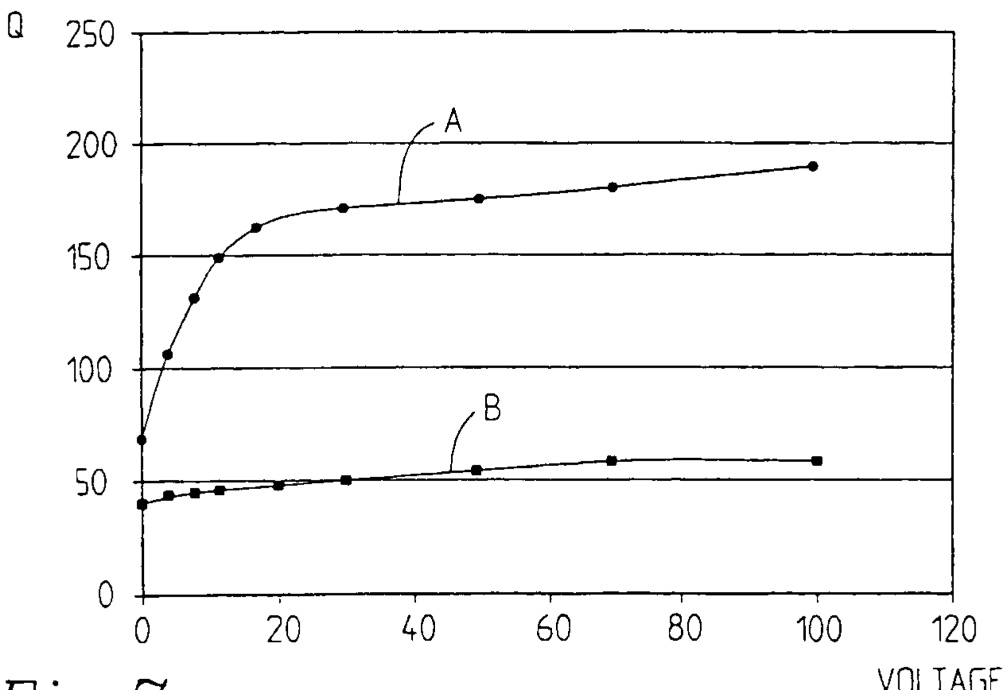
Fig. 6


Q
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200
150
100
50
0
A
B
0
20
40
60
80
100
120
VOLTAGE

Fig. 7

INTERNATIONAL SEARCH REPORT

International application No.
PCT/SE 00/00685

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H01P 1/203, H01P 7/08, H01G 7/06
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H01G, H01P, H01Q

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5524092 A (JEA K PARK), 4 June 1996 (04.06.96) --	1,2,6
A	EP 0518117 A1 (RAMTRON INTERNATIONAL CORPORATION), 16 December 1992 (16.12.92) --	1,6,8,9,14
A	WO 9413028 A1 (SUPERCONDUCTING CORE TECHNOLOGIES, INC), 9 June 1994 (09.06.94), cited in the application --	1-21
A	US 5640042 A (THOMAS E. KOSCICA ET AL), 17 June 1997 (17.06.97), cited in the application -- --------	1-21

☐ Further documents are listed in the continuation of Box C. ☒ See patent family annex.

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Date of the actual completion of the international search	Date of mailing of the international search report
24 August 2000	29 -08- 2000

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Information on patent family members

08/05/00

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PCT/SE 00/00685

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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(71) Applicant: **TELEFONAKTIEBOLAGET LM ERICSSON (publ)** [SE/SE]; S-126 25 Stockholm (SE).

(72) Inventors: **CARLSSON, Erik**; Toltorpsgatan 39C, S-431 39 Mölndal (SE). **PETROV, Peter**; Chalmers Tekniska Högskola, Mikrloelektronik, S-412 96 Göteborg (SE). **VENDIK, Orest**; Korabelostroitelay 42, apt, S.Petersburg, 199155 (RU). **WIKBORG, Erland**; Lundblads väg 3, S-182 33 Danderyd (SE). **IVANOV, Zdravko**; Mölndalsvägen 41, S-412 63 Göteborg (SE).

(74) Agents: **BERGENTALL, Annika** et al.; Cegumark AB, P.O. Box 53047, S-400 14 Göteborg (SE).

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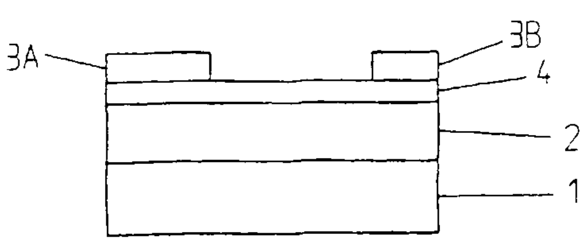
(54) Title: TUNABLE MICROWAVE DEVICES



(57) Abstract: The present invention relates to an electrically tunable device (10), particularly for microwaves. It comprises a carrier substrate (1), conducting means (3A, 3B) and at least one tunable ferroelectric layer (2). Between the conducting means (3A, 3B) and the tunable ferroelectric layer (2) a buffer layer (4) consisting of a thin film structure comprising a non-ferroelectric material is arranged.

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